New Modeling of Switching Devices Considering Power Loss in Electromagnetic Transients Program Simulation

Seung-Tak Kim*, Jung-Wook Park* and Seung-Mook Baek†

Abstract – This paper presents the modeling of insulated-gate bipolar transistor (IGBT) in electromagnetic transients program (EMTP) simulation for the reliable calculation of switching and conduction losses. The conventional approach considering the physical property of switching devices requires many attribute parameters and large computation efforts. In contrast, the proposed method uses the curve fitting and interpolation techniques based on typical switching waveforms and a user-defined component with variable resistances to capture the dynamic characteristics of IGBTs. Therefore, the simulation time can be efficiently reduced without losing the accuracy while avoiding the extremely small time step, which is required in simulation by the conventional method. The EMTP based simulation includes turn-on and turn-off transients of IGBT, saturation state, forward voltage of free-wheeling diode, and reverse recovery characteristics, etc. The effectiveness of proposed modeling for the EMTP simulation is verified by the comparison with experimental results obtained from practical implementation in hardware.

Keywords: Conduction loss, Curve fitting, Electromagnetic transients program simulation, Insulated-gate bipolar transistor, Interpolation, Modeling, Switching loss

1. Introduction

The insulated-gate bipolar transistor (IGBT) is being widely used in many power-electronics based applications such as the pulse-width-modulation (PWM) converters and inverters. Its conduction and switching losses are the main cause of power dissipations in device. Therefore, their exact calculation in simulation is strongly required for the effective design of power electronic circuits and the protection of IGBTs before the actual devices are made in practice. In particular, the accurate prediction of switching losses is extremely important in designing the thermal management system [1, 2]. This is because the temperature of components will be increased by switching losses if the system is designed with high switching frequency to improve its performance.

The most electromagnetic transients programs (EMTPs) can provide the modeling for many power electronic devices in simulation as the form of on-off switches with two-state resistors. This simple representation is sufficiently accurate to simulate electrical behaviors of system. However, in terms of switching loss, the more accurate estimation method is highly required to predict the maximum junction temperatures and overall power converter efficiencies correctly [3]. To achieve this goal, the approaches using the complete model of switching device can be applied. Several detailed models [4-18] including the parasitic capacitances and inductances have been already proposed with a fully analytical treatment or sufficient experimental tests. Unfortunately, these models require the intensive computational efforts. Moreover, the information obtained from practical tests under some specific conditions is not always available in most cases. In other words, because each simulation provides the valid results only for the corresponding case, neither a general conclusion nor a clear physical insight into switching process is easily obtained with these models.

Meanwhile, the other approaches have been proposed by calculating the power loss as the product of current and voltage waveforms. In [9], the type of hysteresis switching is studied considering its switching pattern in a power converter, which affects the power loss. The ideal sinusoidal collector and load currents are used in [10] and [11]. The significant signal conditioning method to estimate losses under aperiodic switching is shown in [12]. The relationships between switching energy and collector current are also considered in [13]. In particular, the more detailed information in switching characteristics is required in [1]. These simulation approaches require many attribute parameters to estimate the switching loss. Moreover, the drop in overall efficiency of system due to the conduction and switching losses is not directly reflected in each simulation time step. Therefore, for the proper modeling compatible with EMTP simulation, the power loss by switching must be efficiently calculated while providing both accuracy and fast simulation time. In addition, the generality of model must be guaranteed without regard to

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the used switching pattern and frequency, and even applied control methods.

This paper proposes the new modeling of IGBT in EMTP simulation for the effective power loss calculation while overcoming the limitations of conventional methods. Firstly, the curve fitting and interpolation techniques for estimation are applied based on the information of performance curves in datasheet. Then, the user-defined component with variable resistances is developed to capture the dynamic characteristics of IGBTs. Furthermore, the proposed modeling enables to calculate the overall efficiency of system correctly in every simulation time interval. This results in reducing the simulation time when compared to the relatively smaller time step used in conventional methods.

The proposed modeling is implemented by using the power system computer aided design/electromagnetic transients including DC (PSCAD/EMTDC®) software, which is the one of widely used EMTP programs. Then, its effectiveness in simulation is verified by comparing the experimental results obtained from practical system in hardware, which is the photovoltaic (PV) grid-connected power conditioning system (PCS) of 3 kW.

2. Proposed Modeling of Switching Devices for EMTP Simulation

The accurate modeling of switch devices such as IGBT and diode is described in this section with the consideration of their conduction and switching losses. As an example of its application, the total loss of semiconductors in PM75CLA120, which is the commercial intelligent power module (IPM) used in a three-phase inverter of 75 A and 1200 V, is calculated. This IPM includes a protection circuit for short-circuit current, overheating, and low-voltage. Also, it has six IGBTs and six freewheeling diodes. The more detailed information is given in its datasheet [14]. Nevertheless, it is insufficient to estimate its power loss exactly.

As mentioned previously, the proposed modeling method uses the numerical values obtained by the polynomial curve fitting with respect to performance curves in the datasheet of PM75CLA120. Then, it is ready to fully capture the internal characteristics of IGBTs and diodes.

2.1 Structure of proposed modeling

In general, the switching devices such as thyristor, diode, IGBT, and breaker, etc., used in the EMTP are modeled with two-state resistors, which change their state from on-resistance to off-resistance, or vice versa. On the other hand, its accuracy in a switching event depends on the time step in the EMTP simulation. In this case, the interpolation technique can solve the inaccurate modeling problem, which might be caused by the fixed time step. This is because it allows the solution to go back to any instant in time required to model the switching behavior. For example, a diode would cause the solution to back in time when its current was exactly zero before it switches, whereas the fixed time step would switch the diode state in the next time step after the zero crossing [15-16]. The structure of proposed modeling to apply the interpolation technique is shown in Fig. 1.

Likewise the conventional model, the proposed model still uses the original switching on and off signals of controller, as shown in Fig. 1. Note that the power loss is related with the voltage drop across switching device and the magnitude of current flowing through that element. Therefore, to take into account this, the proposed model considers the power consumed in circuit by current and voltage. In other words, it uses two variable resistors, $r_s$ and $r_{sh}$ in Fig. 1(b), to model the above power consumption. The series-connected resistor, $r_s$ accounts for the power consumed by the conduction and turn-on switching losses. Also, the parallel-connected resistor, $r_{sh}$ is used to model the turn-off switching loss and the reverse recovery loss only if the device is the type of diode.

2.2 Switching loss estimation of IGBT

Typical switching waveforms of IGBT are shown in Fig. 2 [17]. One is the current, $i_c$ flowing through the switch, and the other is the voltage across switch, $v_{cc}$. When the signal of switch operation, $s_{sw}$ in Fig. 1(b) is applied, the turn-on and turn-off switching losses occur at the corresponding switching times, $t_{on}$ and $t_{off}$, respectively. For their calculation, the turn-on voltage, $v_{cc, on}$, the turn-on
current, \(i_{\text{on}}\), the turn-off voltage, \(v_{\text{cc, off}}\), and the turn-off current, \(i_{\text{off}}\) are measured. These measurements vary depending on the operation of switch and the status of circuit.

The process in EMTP to obtain the measurements, \(v_{\text{cc, on}}\), \(i_{\text{on}}\), \(v_{\text{cc, off}}\), and \(i_{\text{off}}\) measured inside the model is shown in Fig. 3. After the operation of switch is changed, there is the delay for turn-on and turn-off switching times, or for reverse recovery time, \(t_{\text{rr}}\) of diode. Then, the measurements are properly obtained by using the sample and hold (S/H), the mono-stable function, and two edge detectors. Table 1 gives the information for input and output of each function in Fig. 3.

As shown in Fig. 4(a), the turn-on switching loss is easily obtained from the datasheet at \(v_{\text{cc, rated}}\) of 600 V. Also, the collector current shows the sudden change when it is about 40 A. Note that the PV PCS of 3 kW used in experimental test operates with its rated current of 13.6 A. Therefore, this abrupt change does not cause any problems to verify the performance of proposed modeling. If the current is over than 40 A, the higher order polynomial might be required to estimate this sudden change. In this paper, the fourth-order polynomial is used for the appropriate accuracy.

![Fig. 2. Typical switching waveforms of IGBT.](image)

![Fig. 3. The process in EMTP to obtain the measurements.](image)

![Fig. 4. Estimation results of turn-on and turn-off switching losses: (a) turn-on; (b) turn-off.](image)

When the junction temperature, \(T_j\) is 25 °C and 125 °C, the fourth-order polynomials to approximate the turn-on switching loss, \(E_{\text{loss, ton}}(\text{°C})\), are represented as (1) and (2), respectively.

\[
E_{\text{loss, ton}}(25\text{°C}) = \sum_{k=0}^{4} c_{\text{ton, }k}(25\text{°C}) \cdot i_{\text{on}}^k
\]

\[
E_{\text{loss, ton}}(125\text{°C}) = \sum_{k=0}^{4} c_{\text{ton, }k}(125\text{°C}) \cdot i_{\text{on}}^k
\]

where the coefficients, \(c_{\text{ton, }k}\), vary depending on the junction temperature to reflect its characteristics. And, it is assumed that the switching losses have the approximately linear relationship with junction temperature [18]. Therefore, they can be expressed with the linear form as (3),

\[
c_{\text{ton, }k} = d_{1, \text{ton, }k} \cdot T_j + d_{2, \text{ton, }k}
\]

where the \(T_j\) is 25 °C or 125 °C. Also, the coefficients, \(d_{1, \text{ton, }k}\) and \(d_{2, \text{ton, }k}\) are calculated as,

\[
d_{1, \text{ton, }k} = \frac{c_{\text{ton, }k}(125\text{°C}) - c_{\text{ton, }k}(25\text{°C})}{125 - 25}
\]


\[ d_{2 \text{on}_k} = c_{\text{on}_k}(25°C) - 25 \cdot d_{1 \text{on}_k} \\
\text{or } c_{\text{on}_k}(125°C) - 25 \cdot d_{1 \text{on}_k} \tag{5} \]

Because the power is proportional to the square of voltage, the eqns. (1) and (2) must be modified by considering the \( v_{cc \text{on}} \). Then, the estimated \( E_{loss \text{on}} \) is represented as

\[ E_{loss \text{on}} = \frac{v_{cc \text{on}}^2}{v_{cc \text{rated}}} \sum_{k=0}^{4} (d_{1 \text{on}_k} \cdot T_j + d_{2 \text{on}_k}) \cdot t_{i \text{on}} \tag{6} \]

Moreover, the resistance must be also considered for the estimation of \( E_{loss \text{off}} \). Because the time step, \( \Delta t \) is fixed in the EMTP simulation, the switching losses are consumed in several time steps during the turn-on and turn-off times. In addition, they might be calculated incorrectly due to the instantaneous voltage across switch. Therefore, the proposed modeling uses the fixed resistance for only single switching event.

Thereafter, the value of variable series-connected resistor, \( r_s \) is computed by the estimated \( E_{loss \text{on}} \) with the maximum and minimum values of resistor connected in parallel, \( r_{sh} \) and internal on-state resistor of switch, respectively. If \( v_{cc \text{on}} \) is increased, \( E_{loss \text{on}} \) is also increased by (6). That is, when the switch is turned on, the value of \( r_s \) in the proposed modeling is determined as

\[ r_s = \frac{v_{cc \text{on}}^2}{E_{loss \text{on}}} \frac{t_{i \text{on}}}{\Delta t}, \quad r_{sh} = r_{\text{max}} \tag{7} \]

where the value of \( r_{\text{max}} \) must be sufficiently large to make the branch with \( r_{sh} \) open. Therefore, the \( r_{\text{max}} \) of 1 MΩ is used in this study.

In the similar manner, when the switch is turned off, the estimated \( E_{loss \text{off}} \) and the value of variable resistor connected in parallel, \( r_{sh} \) are computed by (8) and (9), respectively. Note that the maximum value of \( r_s \) is applied in this case. Finally, the good estimation results are shown in Fig. 4(b).

\[ E_{loss \text{off}} = \frac{v_{cc \text{off}}^2}{v_{cc \text{rated}}} \sum_{k=0}^{4} (d_{1 \text{off}_k} \cdot T_j + d_{2 \text{off}_k}) \cdot t_{i \text{off}} \tag{8} \]

\[ r_s = r_{\text{max}}, \quad r_{sh} = \frac{v_{cc \text{off}}^2}{E_{loss \text{off}}} \frac{t_{i \text{off}}}{\Delta t} \cdot \Delta t \tag{9} \]

### 2.3 Switching loss estimation of diode

Differently from the IGBT, the switching state of diode is determined depending on the direction of current. Based on the interpolation technique described in sub-section II-A, its current can become zero without regard to the simulation time step. Therefore, the operating state of diode can be accurately reflected in the proposed modeling.

For the modern fast-recovery diode used with IGBTs, its turn-on loss is negligible because it is very small (less than 1 %) when compared to its turn-off loss [19]. However, the reverse recovery behavior during the turn-off time causes the considerable loss. Also, it affects the turn-on loss of IGBT. In general, the turn-off loss data of diode is not given in the datasheet. Therefore, the characteristics of its reverse recovery switching loss are investigated and then estimated.

The typical switching waveforms of diode are shown in Fig. 5. With the information of reverse recovery current \( (i_{rr}) \) and reverse recovery time \( (t_{rr}) \) from the datasheet as shown in Fig. 6, its switching loss is estimated. Similarly to the previous estimation method for the IGBT, the characteristic curves of diode are approximated by (10) and (11) with the fourth-order polynomial in the ranges, where the value of correct recovery current is less than 40 A. This is because the sudden change of temperature occurs at that point as the (turn-on) case of IGBT.

\[ i_{rr} = \sum_{k=0}^{4} (d_{1 \text{rr}_k} \cdot T_j + d_{2 \text{rr}_k}) \cdot t_{i \text{rr}} \tag{10} \]

\[ t_{rr} = \sum_{k=0}^{4} (d_{1 \text{rr}_k} \cdot T_j + d_{2 \text{rr}_k}) \cdot t_{i \text{rr}} \tag{11} \]

where the \( T_j \) is still 25 °C or 125 °C.

Then, the estimated \( E_{loss \text{off}} \) [20] and the value of associated resistor, \( r_{sh} \) are represented as

\[ E_{loss \text{off}} = \frac{0.25 \cdot t_{rr} \cdot i_{rr} \cdot v_{cc \text{off}}^2}{v_{cc \text{rated}}^2} \tag{12} \]

\[ r_s = r_{\text{max}}, \quad r_{sh} = \frac{v_{cc \text{off}}^2}{E_{loss \text{off}}} \frac{t_{i \text{off}}}{\Delta t} \cdot \Delta t \tag{13} \]

Note that the maximum value of \( r_s \) is still applied in this case. It is known form the results in Fig. 6 that the proposed method still provides the good estimation performance for \( i_{rr} \) and \( t_{rr} \) of diode. As the result, the proposed modeling for its reverse recovery switching loss is acceptable.

### 2.4 Conduction loss estimation of IGBT and diode

The nonlinear characteristics of conduction loss of IGBT

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and diode cannot be fully captured by the conventional modeling with the only fixed internal on-state resistor. To overcome this limitation, the proposed modeling estimates the nonlinear relationships between voltage drop across the switch and current flowing through that device. These relationships are shown in Fig. 7.

One is the corrector-emitter saturation voltage characteristics to the collector current of IGBT. The other is the collector recovery current characteristics to the corrector-emitter voltage of diode.

Then, the collector-emitter voltage, $v_{ce}$ is approximated by the same fourth-order polynomial as the previous cases. In other words, the $v_{ce}$ is estimated by (14), and the value of associated resistor, $r_s$ is calculated by (15).

$$v_{ce} = \sum_{k=0}^{4} (d_{1_{\text{ce}}, k} \cdot T_j + d_{2_{\text{ce}}, k}) \cdot i^k,$$

$$E_{\text{loss-cond}} = v_{ce} \cdot i_c \quad (14)$$

$$r_s = \frac{v_{ce}}{i_c}, \quad r_{sh} = r_{\text{max}} \quad (15)$$

The results in Fig. 7 show the good estimation performance for conduction loss, $E_{\text{loss-cond}}$ by the proposed modeling.

2.5 Implementation of proposed modeling in EMTP

For the most cases that require several switching devices in a power converter, the proposed modeling is implemented...
in the EMTP simulation by the procedure as shown in Fig. 8. Firstly, it starts by setting up the electric branch structure as shown in Fig. 1(b) with the series and parallel resistors. Then, it measures the current, voltage, and junction temperature of switching device. For the diode, it estimates the $i_{c,off}$ and $t_{r,off}$ by (10) and (11), respectively, after obtaining the $i_{c,off}$. Thereafter, it also measures $v_{cc,off}$, $v_{cc,off}$, and $i_{c,on}$. It decides whether it operates in turn-on or turn-off time.

For the diode in turn-off time, its reverse recovery switching loss, $E_{loss_drr}$ is estimated by (12). For the case of turn-on time, it calculates the conduction loss, $E_{loss_cond}$ by estimating the collector-emitter voltage, $v_{cc}$ by (14). On the other hand, for the case of IGBT, its turn-on switching loss, $E_{loss_ton}$ and turn-off loss, $E_{loss_toff}$ are estimated by (6) and (8), respectively. Also, the values of associated resistors, $r_{sh}$ and $r_s$, are calculated correspondingly to their suitable cases.

### 3. Simulation Results

The performances of proposed modeling are evaluated by time-domain simulation based on the PSCAD/EMTDC® software. The PV PCS of 3 kW is tested with a fully-detailed topology model with proposed switching device modeling method, which consists of a DC/DC boost converter and a full-bridge PWM inverter with the switching frequency of 20 kHz. It is shown in Fig. 9.

When the junction temperature is 60 °C, the simulation results of IGBT in the lower side and diode in the upper side of the DC/DC boost converter are shown in Figs. 10 and 11, respectively. The switch signal has the duty ratio of 0.275, and the turn-on signal of IGBT is applied at $t = 10 \mu s$.

In Fig. 10(a), the voltage of $v_{cc}$ drops after the delay of 1.2 µs from $t = 10$ µs when the simulation time-step is 0.3 µs. In contrast, it shows the delay of 2 µs when the simulation time-step is 2 µs. The circuit simulation including the voltage of $v_{cc}$ drop is carried out in accordance with the time step. This means that the turn-on switching time mainly depends on the simulation time-step. Also, the current of IGBT and power loss (sum of conduction and switching losses) are shown in Figs. 10(b) and 10(c), respectively. In Fig. 10(c), the energy of power loss for 0.3 µs and 2 µs time-steps are 1.52 mJ and 1.59 mJ per switching period, respectively. Note that they are very close. On the contrary, the peak values of current and power loss (particularly for the turn-on switching loss) are
different depending on the simulation time-step.

When the IGBT is off, the voltage across diode becomes zero. If the IGBT turns on after the delay depending on simulation time-step, the state of diode is changed from closed to open, as shown in Fig. 11(a). It is known from the result of Fig. 11(b) that its current is exactly zero by the interpolation technique when the diode is completely turned off. Also, the result of Fig. 11(c) show that the proposed modeling generates the similar energies of reverse recovery switching loss, which are 0.71 mJ and 0.66 mJ for the cases of time-steps of 0.3 µs and 2 µs, respectively. Note that these values are close even though their peak values are very different.

For the PWM inverter in Fig. 9, the current and power loss responses of upper left IGBT and diode are shown in Fig. 12. The peak value of current flowing the IGBT is the same as that of half waveform of current flowing to grid side, which is the output of PV PCS. Also, it is observed that the power loss of IGBT depends on the magnitude of current. The similar results are applied to the diode except that the current flowing the diode is phase-shifted by 180°, and the power loss consumed in diode is less than that of IGBT.

The more detailed power loss analysis of PV PCS is shown in Fig. 13. It is observed that the relationship between power loss and output power of system is linear without regard to the type of converter and switch. Also, the power loss of inverter is about two times that of boost converter. This is because two switching devices in inverter operate in one turn-on and turn-off switching action. Similarly to the result in Fig. 12(b), the result of Fig. 13(b) show that the power loss of IGBT is much greater than that of diode.

4. Experimental Results

4.1 Hardware implementation

The effectiveness of proposed modeling for the EMTP simulation is verified by the experimental test implemented in hardware as shown in Fig. 14(a). The photograph of real PV PCS of 3 kW is shown in Fig. 14(b).

The DC/DC boost converter and DC/AC inverter are controlled independently with the switching frequency of 20 kHz. In particular, their PWM controllers are implemented by the digital signal processor (DSP) of TMS320F2812. The boost converter is operated with the maximum-power-point-tracking (MPPT) algorithm based on the perturbation and observation (P&O) method [21]. The inverter is connected to a commercial electrical outlet of 220 V. Also, it controls the DC capacitor voltage in real-time. To operate the DSP, control circuit, switching devices, and sensors, the switching mode power supply (SMPS) is applied to adjust their suitable voltages and to supply the power. The specifications of PV PCS are given in Table 2.

As shown in Fig. 14(c), the voltage and current outputs are measured by using the digital phosphor Tektronix oscilloscope of the DPO4054. Also, its output power is measured from the power analysis application module of DPO4PWR, which is attached to the oscilloscope of DPO4054.

4.2 Experimental results

The experimental results of system efficiency under various operating conditions are shown in Fig. 15. The
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Fig. 14. Hardware implementation: (a) hardware set-up for experimental test, (b) PV PCS of 3 kW, (c) experimental results: voltage and current outputs.

Table 2. Specifications of PV PCS

<table>
<thead>
<tr>
<th>System</th>
<th>Parameters</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>PV simulator</td>
<td>Output current range</td>
<td>0–20 A</td>
</tr>
<tr>
<td></td>
<td>Output voltage range</td>
<td>10–500 V</td>
</tr>
<tr>
<td>DC/DC boost converter</td>
<td>Rated power</td>
<td>3 kW</td>
</tr>
<tr>
<td>DC/AC inverter</td>
<td>MPPT algorithm</td>
<td>P&amp;O</td>
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<tr>
<td></td>
<td>Rated power</td>
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<tr>
<td></td>
<td>Rated DC voltage</td>
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<tr>
<td></td>
<td>Switch frequency</td>
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<td>Type</td>
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<tr>
<td></td>
<td>Rated power</td>
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</table>

Table 3. Comparison Results of RMSE and Error in European efficiency

<table>
<thead>
<tr>
<th>Simulation method</th>
<th>RMSE by (16)</th>
<th>Absolute error in $\eta_{euro}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>Conventional modeling</td>
<td>7.726</td>
<td>7.723</td>
</tr>
<tr>
<td>Proposed modeling</td>
<td>1.162</td>
<td>0.656</td>
</tr>
</tbody>
</table>

Fig. 15. The comparison of experimental and simulation results for system efficiencies.

The large difference from the measurements. In contrast, the system efficiency is calculated accurately with the simulation by proposed modeling. This verifies that the proposed method estimates the power loss correctly.

For the numerical comparison, the root-mean-square error (RMSE) and the European efficiency, which is the weighted conversion efficiency and used for the particular application such as this PV PCS, are considered. Firstly, the RMSE is computed as

$$RMSE = \sqrt{\frac{\sum_{i=1}^{n} (\eta_{measured,i} - \eta_{model,i})^2}{n}}$$  (16)

where $n$ is the number of samples, $\eta_{measured,i}$ is the efficiency measured in experimental test, and $\eta_{model,i}$ is the efficiency obtained in simulation test. Then, it is calculated in the range from 300 W to 3.2 kW with the increase of 100 W.

On the other hand, the European efficiency is the useful comparative tool in this study because the output of PV PCS can be affected by the external condition such as weather. It aims to approximate the integral of conversion efficiency versus time over the entire day. The European efficiency, $\eta_{euro}$, is defined as follows [22], [23]:

$$\eta_{euro} = 0.03\eta_{5\%} + 0.06\eta_{10\%} + 0.13\eta_{20\%} + 0.10\eta_{30\%} + 0.48\eta_{50\%} + 0.20\eta_{100\%}$$  (17)

where $\eta_{i\%}$ is the conversion efficiency at $i\%$ of the rated power output.

The comparison results for the RMSE in (16) and absolute error in $\eta_{euro}$ are given in Table III. It is clearly observed that the proposed modeling outperforms the conventional one (which is carried out by the built-in
function of PSCAD/EMTDC® software with the only two-state resistors) significantly for the computation of efficiency. In particular, for the \( \eta_{\text{error}} \), the proposed modeling provides the very accurate performance with the error of 0.656 %, which is less than 1 %.

5. Conclusions

This paper proposed the new modeling of insulated-gate bipolar transistor (IGBT) and diode to estimate their switching and conduction losses by using the interpolation and curve fitting techniques. The proposed modeling was implemented for the effective use in the electromagnetic transients program (EMTP) simulation while avoiding the extremely small time step required to capture the full characteristics of switching devices.

Both simulation and experimental tests were carried out to verify the usefulness of proposed modeling. In particular, the photovoltaic (PV) grid-connected power conditioning system (PCS) of 3 kW was implemented in hardware for experimental test. The given results verified that the proposed method estimates the power loss of switching devices very accurately, and therefore it can be preferably used to calculate the system efficiency in the EMTP simulation.

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